



### ICECST

2nd International Conference on Electronic Circuits and Signaling Technologies

**ICECST 2025** 

- Malaysia
- icecst.conf@gmail.com

23-25, October 2025





Web site:https://icecst.com/

# About college

Lincoln University College (LUC), located at Petaling Jaya, Malaysia was established in the year 2002 as Lincoln College (LC) and later upgraded to Lincoln University College in the year 2011. Lincoln University College is one of the premier private institutions of higher education approved by the Ministry of Higher Education and Malaysian Qualifications Agency (MQA- National Accreditation Board).

The University College is also crowned with 5 Star ranking by the Ministry of Higher Education, Malaysia in 2017. Lincoln University College is listed among the top nine Malaysian universities according to the Times Higher Education (THE) University Impact Rankings 2019. LUC is also an ISO 9001:2015 certified academic institution. Lincoln University College is also the associate member of the 'Association of Indian Universities (AIU)', Association of Commonwealth Universities (ACU), London and a member of 'International Association of Universities (IAU)', Paris.

### About conference

The 2nd International Conference on Electronic Circuits and Signaling Technologies [ICECST 2025] has been planned to highlight the rapidly emerging technologies related to advanced electronic circuits and signal processing technologies. The conference will provide an interdisciplinary platform for researchers to present and discuss about the state-of-the-art research innovations, trends, and challenges encountered and solutions adopted in the fields of intelligent electronic circuits and signal processing technologies. The signals, communications and embedded domain covers the analog and digital signals and systems and embedded system by using the analytical and digital tools for the analysis of deterministic signals and applications to digital signal processing and digital/analog communications. The 2nd International Conference on Electronic Circuits and Signaling Technologies [ICECST 2025] is proud to host a global gathering of novel research expertise in advanced characterization techniques in the highly complex field of signal processing and information communications, which brings together researchers, practitioners, and scientists to discuss about the latest methods, research developments, and future opportunities. Moreover, ICECST 2025 has been designed to leverage a unique opportunity to gain state-of-the-art research insights through knowledge exchange on the convergence between the smart and advanced Electronic Circuits and Signaling Technologies.

### Call for Papers

### Circuits and Embedded Systems

- Digital Circuits and FPGA based Designs
- Memory Circuits and System Design
- Wearable Electronics and Sensors
- MEMS/NEMS Devices and Applications
- Nanophotonics, Nanoelectronics and Hybrid Platforms
- Semiconducting Electronic Material and Devices
- Sustainable Electronic Design
- Circuit Reliability and Fault Tolerance
- Computer Aided Design and Verification
- Device Technology, Modeling and Simulation
- Embedded for IoT and Signal Processing Models
- Embedded for Al, ML, and Future Technologies
- Emerging CMOS Technologies
- Microsystems
- Sensors and Sensing Systems
- Biomedical and Bio-inspired Systems
- VLSI Systems and Applications
- Networked Embedded Systems

### Signal Processing Technologies

- Design and Implementation of Signal Processing Systems
- Novel Signal processing Theories and Methods
- Nonlinear Signal Processing
- Pattern Recognition, Analysis and Classification
- Optimization Techniques
- Speech and Language Processing
- Statistical Signal Processing
- Multimedia Signal Processing
- Multichannel Signal Processing
- Machine Learning/Deep Learning
- Signal Processing for VLSI
- Computer Vision and Robotics with Signal Processing Models
- Signal Processing Over Graphs and Networks
- Biomedical and Bio-Inspired Signal Processing
- Speech Information Processing: Recognition
- Diversity and Space-time Techniques

## Conference Committee

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Dr. Bikash Sharma, Sikkim Manipal Institute of Technology, India.

Dr. Kanika Singh, American Bureau of Shipping, Republic of Korea (South Korea).

Dr. João Manuel R. S. Tavares, Universidade do Porto, Portugal.

Dr. Ram Palanisamy, StFX University, Canada.

Dr. Anshul Agarwal, National Institute of Technology, Delhi, India.

Dr. Alexandros-Apostolos A. Boulogeorgos, Aristotle University of Thessaloniki, Greece.

Dr. Nur Ashida binti Salim, College of Engineering, Universiti Teknologi MARA, Malaysia.

Dr. Subrata Biswas, SMIEEE (USA), Netaji Subhash Engineering College, India.

Dr. Sivapriyan. R, Sir M Visvesvaraya Institute Of Technology, Bangalore, India.

Dr.V.Lakshmi Devi, Sri Venkateswara College of Engineering, India.

#### **Local Organizing Committee**

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## Keynote Speakers

Dr. M. V. Reddy

Senior Researcher, Institute of Research Hydro-Québec, Centre of Excellence in Transportation Electrification and Energy Storage, Hydro-Québec, Canada.

Dr. Anshul Agarwal Associate Professor, Electrical Engineering, National Institute of Technology, Delhi, India.

Prof. Ved Ram Singh National Physical Laboratory (NPL), New Delhi, India

#### **Publication**

Papers accepted and presented at the conference will be recommended for inclusion in IEEE Xplore, subject to the acceptance of the review committee, immediately after their presentation at the conference.

#### Contact

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#### Venue

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